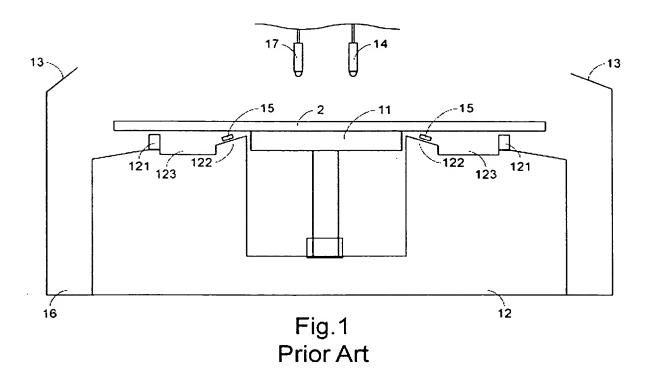
Chun-Pok Leung, Reg. No. 41,405, (650) 326-2400 Titled: Developing Method for Semiconductor Substrate Inventor(s): Chen Tang Lin, et al. Filed: April 1, 2004 Dckt. No. 17620R-003400US Sheet 1 of 4



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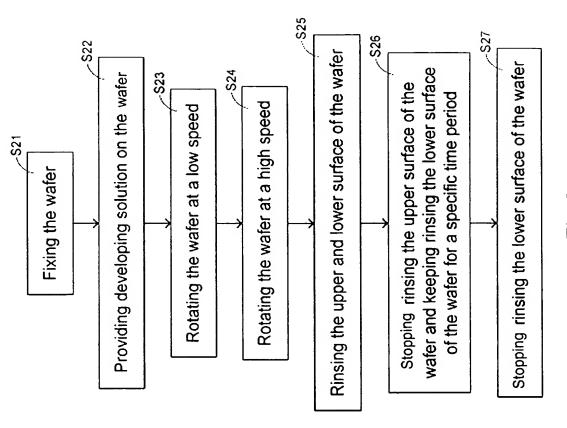


Fig.2

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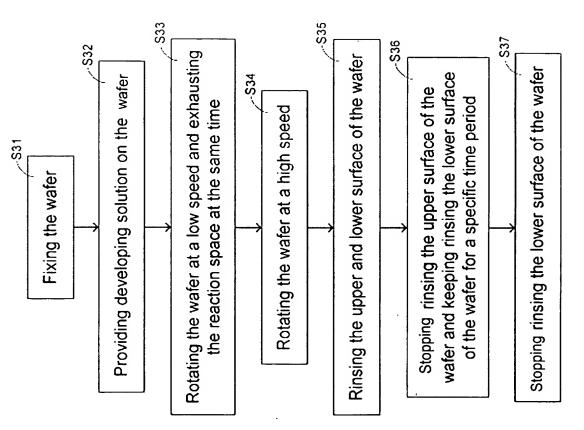


Fig. 3

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